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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/078,489 02/21/2002 PAT 6,657,260 .

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-056063 02/28/2001  
 JAPAN 2001-302687 09/28/2001

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 18	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature: <i>M. M.</i> Initials: <i>M</i>				

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## TITLE

Semiconductor device and manufacturing method thereof

☐ All Fees☐ 1.16 Fees ( Filing )

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